Freeform Search

Database: US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Term: L41 and (temperature)

Display: 10 **Documents in Display Format:** - **Starting with Number** 1

Generate: ○ **Hit List** ◉ **Hit Count** ○ **Side by Side** ○ **Image**

Search Clear Interrupt

Search History

DATE: Thursday, March 30, 2006 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L42	L41 and (temperature)	15	L42
L41	bias circuit adj1 substrate	42	L41
L40	L39 and (temperature)	43	L40
L39	separate bias circuit	103	L39
L38	L37 and (temperature)	32	L38
L37	(bias) adj5 (detach$4)	198	L37
L36	(bias circuit) adj5 (detach$4)	1	L36
L35	bias circuit made separat$3	0	L35
L34	(bias circuit) adj5 (separate chip)	0	L34
L33	(bias circuit) adj5 (substrate or chip or module or integrated circuit or printed circuit board or IC or spaced apart) and (temperature adj5 sensor)	31	L33
L32	(bias circuit) adj5 (substrate or chip or module or integrated circuit or printed circuit board or IC or spaced apart)	875	L32
L31	bias circuit adj5 substrate or spaced apart	734511	L31
L30	spaced adj5 bias circuit	5	L30

L29	bias circuit on substrate	0	L29
L28	L27 and L1	15	L28
L27	(bias$3 adj5 substrate)	17309	L27
DB=USPT; PLUR=YES; OP=ADJ			
L26	5066140.pn.	1	L26
L25	4331888.pn.	1	L25
L24	5213416.pn.	1	L24
L23	5195827.pn.	1	L23
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L22	L1 and (reference diode)	21	L22
DB=PGPB; PLUR=YES; OP=ADJ			
L21	20040233968 and (insulat$3 or isolat$3 or air)	0	L21
L20	20040233968 and (82)	0	L20
DB=USPT; PLUR=YES; OP=ADJ			
L19	6991368.pn. and (battery)	0	L19
L18	374/$.ccls. and (Pompei)	167	L18
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L17	L1 and (multiplex$3 two currents) or (switch$3 two currents)	100	L17
L16	L1 and (multiplex$3 adj5 currents) or (switch$3 adj5 currents)	201560	L16
L15	L1 and (multiplex$3 or switch$3) same (time or time division)	3016	L15
DB=USPT; PLUR=YES; OP=ADJ			
L14	L1 and (two current same sequen$5)	3	L14
L13	L1 and (current adj sequen$5)	5	L13
L12	L11 and (kunst)	4	L12
L11	374/$.ccls. and (verbitsky)	374	L11
L10	6612738.pn.	1	L10
L9	5982221.pn.	1	L9
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ			
L8	chip adj5 temperature	14870	L8
L7	on chip temperature	0	L7
L6	L1 and (on chip temperature)	0	L6
L5	L4 and (temperature or thermal)	187	L5
L4	L3 and (first substrate or first integrated circuit or first chip or first IC)	546	L4
L3	327/$.ccls.	108297	L3
L2	L1 and (first substrate or first integrated circuit or first chip or first IC)	57	L2
L1	374/$.ccls.	29045	L1

END OF SEARCH HISTORY